



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al

Art Unit : 1765

Serial No. : 08/690,747

Examiner : Robert Kunemund

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Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Commissioner for Patents

Washington, D.C. 20231

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INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed. An English translation of the non-English document is enclosed.

This statement is being filed before the mailing of a first Office action after the filing of a request for continued examination. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 2/21/01

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231

Date of Deposit February 21, 2001Signature Veronica WhalenTyped or Printed Name of Person Signing Certificate Veronica Whalen